

[illegible]

PATENT NUMBER

## U.S. UTILITY Patent Application

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 Q.A.  
 SCANNED

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/910904	F	438	692	1765	Unverz. Erenini

## APPLICANTS

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# ETIL

# Planarizing method of semiconductor wafer and apparatus thereof

PTO-2040  
12/09

## ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	DRAWINGS			CLAIMS ALLOWED	
	<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print Fig.</b>	<b>Total Claims</b>	<b>Print Claim for O.G.</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____	(Assistant Examiner) (Date)				
				<b>ISSUE FEE</b>	
				<b>Amount Due</b>	<b>Date Paid</b>
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	(Primary Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	
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